

IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Original): A multi-layer printed wiring board loaded with an electronic component and having external terminals, wherein the external terminals are disposed on both faces.

Claim 2 (Original): A multi-layer printed wiring board loaded with an electronic component and having external terminals, wherein a bored portion for accommodating an electronic component is provided in a mounting area,

the external terminals are disposed on both faces.

Claim 3 (Currently Amended): The multi-layer printed wiring board according to claim 1 or 2 wherein the external terminal on the opposite face is disposed off just below the external terminal on the one face.

Claim 4 (Currently Amended): The multi-layer printed wiring board according to claim [[1-3]] 1 wherein the external terminal is connected to a via hole in stacked structure and the via hole connected to the external terminal is deflected from the via hole in an adjacent layer in terms of their center line.

Claim 5 (Currently Amended): The multi-layer printed wiring board according to claim [[1-4]] 1 wherein the multi-layer printed wiring board is formed by overlaying a single side or double side circuit board in which non-through holes formed in insulating material are filled with conductive material.

Claim 6 (Original): The multi-layer printed wiring board according to claim 5 wherein the single side or double side circuit boards are connected to each other through a conductive bump formed on the conductive material filled in the non-through holes.

Claim 7 (Original): A multi-layer printed wiring board in which a mounted electronic component is wire-bonded from a bonding pad,

a substrate being so constructed that a conductor circuit is formed on a single side or double sides of insulation material thereof and the non-through hole leading to the conductor circuit is filled with conductive material,

the conductor circuit just above the non-through hole is used as the bonding pad.

Claim 8 (Original): A multi-layer printed wiring board in which a mounted electronic component is wire-bonded from a bonding pad,

a substrate being so constructed that a conductor circuit is formed on a single side or double sides of insulation material thereof and the non-through hole leading to the conductor circuit is filled with conductive material and

the conductor circuit just above the non-through hole is used as the bonding pad,
while the non-through hole is disposed just below the bonding pad.

Claim 9 (Original): A multi-layer printed wiring board in which a mounted electronic component is wire-bonded from a bonding pad,

a substrate being so constructed that a conductor circuit is formed on a single side or double sides of insulation material thereof and the non-through hole leading to the conductor circuit is filled with conductive material, the substrates being overlaid through conductive bump formed on conductive material filled in the non-through hole,

the conductor circuit just above the non-through hole being used as the bonding pad.

Claim 10 (Original): A multi-layer printed wiring board in which a mounted electronic component is wire-bonded from a bonding pad, a substrate being so constructed that a conductor circuit is formed on a single side or double sides of insulation material thereof and the non-through hole leading to the conductor circuit is filled with conductive material, the substrates being overlaid through conductive bump formed on conductive material filled in the non-through hole, the non-through hole being disposed just below the bonding pad by using the conductor circuit just above the non-through hole as the bonding pad.

Claim 11 (Currently Amended): The multi-layer printed wiring board according to claim [[7-10]] 7 wherein the bonding pad is formed in a rectangular shape.

Claim 12 (Currently Amended): The multi-layer printed wiring board according to claim 9 ~~or 10~~ wherein the conductive bump is formed on an opposite face to the conductor circuit of the bonding pad.

Claim 13 (Currently Amended): The multi-layer printed wiring board according to claim [[7-10]] 7 wherein the external terminals are disposed both faces thereof.

Claim 14 (Currently Amended): The multi-layer printed wiring board according to claim [[7-10]] 7 wherein a bored portion for accommodating an electronic component is provided in a mounting area and the external terminals are disposed on both faces thereof.

Claim 15 (Currently Amended): The multi-layer printed wiring board according to claim 13, [[14]] wherein the external terminal of the opposite face is disposed off just below the external terminal and pad of the one face.

Claim 16 (Currently Amended): The multi-layer printed wiring board according to claim ~~4-6, 13, 14~~ 1 wherein a via is formed in the mounting area of the electronic component and metallic layer having a heat radiation function is formed in an adjacent portion.

Claim 17 (Currently Amended): The multi-layer printed wiring board according to claim ~~4-6, 10-14~~ 1 wherein the external terminal is BGA.